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Ultra-thin GaAs single-junction solar cells integrated with a reflective back scattering layer

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This paper reports the proposal, design, and demonstration of ultra-thin GaAs single-junction solar cells integrated with a reflective back scattering layer to optimize light management and minimize non-radiative recombination. According to our recently developed semi-analytical model, this design offers one of the highest potential achievable efficiencies for GaAs solar cells possessing typical non-radiative recombination rates found among commercially available III-V arsenide and phosphide materials. The structure of the demonstrated solar cells consists of an In_{0.49}Ga_{0.51}P/GaAs/In_{0.49}Ga_{0.51}P double-heterostructure PN junction with an ultra-thin 300 nm thick GaAs absorber, combined with a 5 μ m thick Al_{0.52}In_{0.48}P layer with a textured as-grown surface coated with Au used as a reflective back scattering layer. The final devices were fabricated using a substrate-removal and flip-chip bonding process. Solar cells with a top metal contact coverage of 9.7%, and a MgF₂/ZnS anti-reflective coating demonstrated open-circuit voltages (V_{oc}) up to 1.00 V, short-circuit current densities (J_{sc}) up to 24.5 mA/cm², and power conversion efficiencies up to 19.1%; demonstrating the feasibility of this design approach. If a commonly used 2% metal grid coverage is assumed, the anticipated J_{sc} and conversion efficiency of these devices are expected to reach 26.6 mA/cm² and 20.7%, respectively. © 2014 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4878156]

INTRODUCTION

GaAs has the highest demonstrated power conversion efficiency among all types of single-junction solar cells, and is the most promising material for achieving efficiencies that approach the Shockley-Queisser (SQ) limit. Efficiency records of GaAs solar cells have been steadily broken over the past several years.¹⁻³ Most of the GaAs solar cells with high efficiencies reported up to now use exceptionally highquality materials for relatively thick absorbers, and planar structures with smooth top surfaces and flat backside mirrors.^{2–4} Different designs to enhance the efficiency have also been proposed and reported, such as the use of planer thin absorbers.⁵⁻¹¹ In order to increase the efficiency and reduce manufacturing cost by using commonly available materials with routinely achievable quality, it becomes necessary to implement light management structures (e.g., textured surface)¹²⁻¹⁴ to achieve maximum absorption and minimum non-radiative recombination in a thin absorber. Our recently reported theoretical analysis of different optical designs^{5,6,15,16} shows that the use of an ultra-thin (submicron) absorber and a reflective back scattering layer can potentially result in the maximal achievable conversion efficiency for single-junction GaAs solar cells.

In this paper, a solar cell structure consisting of an $In_{0.49}Ga_{0.51}P/GaAs/In_{0.49}Ga_{0.51}P$ double-heterostructure PN junction with an ultra-thin GaAs absorber and a textured $Al_{0.52}In_{0.48}P/Au$ back reflective layer is modeled, designed, and fabricated. Ultra-thin absorbers and textured back reflective layers are chosen to achieve the optimal efficiency using

routinely available materials with non-radiative recombination rates similar to those reported in the literature.^{5,6,17,18} The textured back scattering layer, offering light trapping, makes the ultra-thin absorber optically thick but physically thin, and thus optimizes the tradeoff between maximizing absorption of incoming photons and minimizing non-radiative recombination in the absorber. In addition, the textured interface on the backside of the Al_{0.52}In_{0.48}P scattering layer is separated from the GaAs absorber so that the surface-related non-radiative recombination is minimized, while the light scattering is maximized independently. The devices were fabricated using a substrate-removal flip-chip bonding process, and an anti-reflective (AR) coating comprised of a MgF₂/ZnS double-layer. This work demonstrates the feasibility of the use of ultra-thin absorbers and textured back reflective layers in single-junction solar cells to optimize light management and to minimize non-radiative recombination, and thus achieve high power conversion efficiencies.

DEVICE DESIGN AND MODELING

The overall device structure is shown in Fig. 1(a). The double-heterostructural GaAs PN junction consists of a 300 nm GaAs absorber (p-type emitter and n-type base) sandwiched between two 30 nm doped $In_{0.49}Ga_{0.51}P$ window and back-surface-field (BSF) layers. A textured surface is placed at the backside of the thin-film solar cell to provide optimal scattering of the transmitted light, which is mainly at red and infrared wavelengths. The reflective back scattering



FIG. 1. Schematic layer structure of (a) the finished $In_{0.49}Ga_{0.51}P/GaAs/In_{0.49}$ Ga_{0.51}P double-heterostructural solar cell with an Al_{0.52}In_{0.48}P layer for reflective back scattering, and (b) as-grown solar cell wafers.

layer underneath the In_{0.49}Ga_{0.51}P BSF layer is a 5 μ m thick, n-type Al_{0.52}In_{0.48}P layer with a textured surface coated with Au. The entire device is bonded on to a Si carrier substrate using indium. Fig. 1(b) shows the detailed layer structure, including the layer thicknesses and the doping concentrations of the epitaxial wafer prior to device processing.

The optimal thickness of the GaAs absorber is chosen according to simulation using the semi-analytical model^{5,6} that takes into account non-radiative recombination, emittance, and absorptance calculated from the reported absorption coefficients¹⁹ including the tail below the bandgap wavelength (Urbach tail). Fig. 2(a) shows plots of simulated power conversion efficiencies versus absorber thickness of GaAs single-junction solar cells with ideal textured reflective back surfaces providing Lambertian scattering for given Shockley-Read-Hall (SRH) recombination saturation current densities per unit length (J_{SRH}/d) . The Auger recombination is assumed to be zero, which is reasonable for typical GaAs materials. J_{SRH}/d is proportional to the non-radiative recombination rate per unit volume in the GaAs absorber, and is therefore an effective factor for characterizing the quality of GaAs material. The green dashed curve with J_{SRH}/d equaling $10 \text{ A/cm}^2/\mu \text{m}$ represents the lowest SRH recombination rate reported in the literature for GaAs.^{5,6} A maximum efficiency of 30.9% is thus potentially achievable with a 230 nm thick absorber using the proposed structure. The optimal absorber thickness for each structure with a given J_{SRH}/d is the result of a compromise between maximizing the absorption and minimizing the non-radiative recombination loss. The curve of $J_{SRH}/d = 0$ represents the case of no non-radiative recombination present; thus, the efficiency approaches the detailed balance limit asymptotically with the absorber thickness approaching infinity. However, as the SRH recombination saturation current density increases with degrading material quality, the maximum achievable efficiency decreases and there exists a decreasing optimal thickness. This trend reveals the advantage of using ultra-thin absorbers when the material has substantial non-radiative recombination loss, such as in the case of solar cells made of thin-film polycrystalline materials.

Utilizing textured surfaces for light management in GaAs-based single- or multi-junction solar cells is challenging. This is mainly due to the fact that the absorbers in those cells usually have large absorption coefficients and are, therefore, very thin with a typical thickness of only a few

microns. These thin layers make it difficult to texture the absorber surfaces. Texturing becomes even more difficult for ultra-thin GaAs absorbers on the order of a few hundred nanometers. Furthermore, textured absorbers have large surface or interface areas and thus result in significantly increased non-radiative surface/interface recombination. The solution proposed here is to integrate the solar cell with a wide-bandgap back scattering layer coated with a reflective mirror. The key advantage of this design is that the textured surface and the absorber/BSF interface are separated. The textures are fabricated on the thick wide-bandgap light scattering layer, and the surfaces of the ultra-thin GaAs absorber remain planar. Therefore, the non-radiative interface recombination of the GaAs absorber will not be increased. It is essential that the material used for the back scattering layer has a large bandgap to minimize the parasitic optical absorption of the transmitted light, as well as be lattice-matched to GaAs to minimize the number of misfit dislocations formed during epitaxial growth. The simulated power conversion efficiencies of GaAs single-junction solar cells integrated with 5 μ m thick reflective back scattering layers comprised different III-V materials (Al_{0.50}In_{0.50}P, of several Al_{0.80}Ga_{0.20}As, Al_{0.59}Ga_{0.41}As, and Ga_{0.50}In_{0.50}P) are plotted versus absorber thickness in Fig. 2(b). The reported absorption coefficients²⁰⁻²² are used for the various back scattering materials. The best reported value of $J_{SRH}/d = 10 \text{ A/cm}^2/\mu\text{m}$ and a typical Auger recombination saturation current density per unit length $J_{Auger}/d = 120 \text{ A/cm}^2/\mu\text{m}$ for GaAs are used.⁶ Ideal AR coatings with no reflection loss and no top contact grid coverage are also assumed here. Note that the choice of 5 μ m as the back scattering layer thickness in the modeling is due to growth considerations, which will be discussed below. The figure clearly indicates the benefits of using textured Al_{0.50}In_{0.50}P layer for back scattering, as it offers the largest efficiency among the four candidates due to its widest bandgap and correspondingly low parasitic absorption of the transmitted light. From the simulation, the optimal thickness of the GaAs absorber with the Al_{0.50}In_{0.50}P back scattering layer is 280 nm. It is noted here that the model assumes ideal Lambertian back scattering. A slightly thicker GaAs absorber (300 nm) is chosen in this work to compensate for the non-ideal scattering typically found in practical devices.

A MgF₂/ZnS double-layer AR coating is designed and optimized by minimizing the front surface reflectance



FIG. 2. Simulated power conversion efficiency of GaAs single-junction solar cells (a) versus GaAs absorber thickness for various normalized SRH non-radiative recombination saturation current densities per unit length J_{SRH}/d (A/cm²/ μ m); (b) versus GaAs absorber thickness for four different III-V materials lattice-matched to GaAs integrated as the back scattering layer; (c) simulated reflectance contour R _{total} for MgF₂/ZnS double-layer AR coating on a GaAs solar cell.

calculated by the transfer matrix method. Note that since the thickness of the GaAs absorber is only 300 nm, it is necessary to take into account the GaAs ultra-thin absorber,

In_{0.49}Ga_{0.51}P window, and BSF layers in the transfer matrix calculation. Once each reflection spectrum is calculated using published complex refractive indices,^{19,20} it is weighted against the solar spectrum and then integrated below the GaAs bandgap wavelength (280 nm–870 nm) to produce a total reflectance for every thickness combination of the MgF₂/ZnS double-layer, as shown by the equation below

$$R_{total} = \frac{\int_{280\text{nm}}^{870\text{nm}} R(\lambda) \cdot N_{sun}(\lambda) d\lambda}{\int_{280\text{nm}}^{870\text{nm}} N_{sun}(\lambda) d\lambda},$$
(1)

where R_{total} is the total reflectance of the AR coating, $R(\lambda)$ is the wavelength-dependent reflectance of the AR coating, and $N_{sun}(\lambda)$ is the photon flux per wavelength from solar radiation. Fig. 2(c) depicts the simulated total reflectance (R_{total}) contours versus variable MgF₂ and ZnS thicknesses. The optimal design (96 nm MgF₂/47 nm ZnS pair) is denoted in the figure by the grey dot in the approximate center, where a minimum R_{total} of 1.60% is achieved.

MATERIAL GROWTH AND DEVICE FABRICATION

The solar cell structures shown in Fig. 1(b) were grown using MOCVD at Sumika Inc., a commercial epi foundry. The double-heterostructural n-In_{0.49}Ga_{0.51}P/n-GaAs/p-GaAs/p-In_{0.49}Ga_{0.51}P PN junction was deposited on an Al_{0.9}Ga_{0.1}As sacrificial layer grown on a GaAs substrate. The p-GaAs and n-GaAs layers have thicknesses of 30 nm and 270 nm, and corresponding doping concentrations of 1.25×10^{17} cm⁻³ and 7×10^{16} cm⁻³, respectively. The window layer and BSF layer are In_{0.49}Ga_{0.51}P offering a high quality interface with GaAs.

A 5 μ m thick n-type Al_{0.52}In_{0.48}P layer with a textured surface is grown on top of the second In_{0.49}Ga_{0.51}P layer. The cone-shaped textures on the surface of the Al_{0.52}In_{0.48}P layer were developed during the growth; hence, no additional processing was required to produce the textured surface for light scattering. Several calibration growths of Al_{0.52}In_{0.48}P layers with different thicknesses were carried out to investigate the correlation between texture distribution and the Al_{0.52}In_{0.48}P thickness. Figs. 3(a) - 3(c)show the cross-sectional SEM images of the Al_{0.52}In_{0.48}P surface with thicknesses of $1 \mu m$, $3.8 \mu m$, and $5 \mu m$. When the Al_{0.52}In_{0.48}P thickness was only 1 µm, most of the surface was smooth and sparse discrete cone textures were formed. The 3.8 μ m Al_{0.52}In_{0.48}P layer showed denser surface textures. However, most of the textures were still discrete, with only a few features beginning to connect with each other. For the 5 μ m Al_{0.52}In_{0.48}P layer, all of the texture features are connected with each other to cover the entire surface. Therefore, $5\,\mu m$ was chosen as the thickness of the Al_{0.52}In_{0.48}P back scattering layer. A zoom-in cross-sectional view of a texture cone with a height of 1 μ m and a bottom diameter of around 5 μ m is shown in Fig. 3(c) inset. The scale in the vertical axis is the same as that in the horizontal axis. Fig. 3(d) shows a top-view microscope image of the



FIG. 3. Cross-section SEM images of as-grown Al_{0.52}In_{0.48}P surfaces when the Al_{0.52}In_{0.48}P thickness is (a) 1 μ m, (b) 3.8 μ m, and (c) 5 μ m; (d) top-view microscopic images of as-grown Al_{0.52}In_{0.48}P surface when the Al_{0.52}In_{0.48}P thickness is 5 μ m. Inset in (c) gives a zoomed-in SEM image of a typical cone texture (height ~ 1 μ m, diameter ~ 5 μ m).

as-grown $Al_{0.52}In_{0.48}P$ surface with high-density cone features.

The device processing begins with the as-grown solar cell structures shown in Fig. 4(a). The initial stage was to prepare the back contact and mirror prior to the flip-chip mounting of the device. The commonly used alloyed metal contact (e.g., Ni/Ge/Au/Ti/Au) for n-GaAs has excellent contact conductance but poor reflectance. Conversely, a pure Au layer has excellent reflectance but poor contact conductance. Therefore, in order to maximize the reflectivity of the back mirror and minimize the contact resistance of the back contact, point contacts (Ni/Ge/Au/Ti/Au) were used as shown in Fig. 4(b). Each point contact has a size of $10 \times 10 \,\mu\text{m}^2$ with a distance of $200 \,\mu\text{m}$ between adjacent points. Thus, the total area of the point contacts accounts for only 0.25% of the back surface. The point contacts were made using a standard photolithography, e-beam deposition, and lift-off process. The sample was then annealed using a rapid thermal annealing process at 425 °C to form ohmic contacts. The remaining GaAs back contact layer was then removed using a citric acid solution revealing the Al_{0.52}In_{0.48}P back scattering layer. A Au film was then deposited on the textured Al_{0.52}In_{0.48}P layer to function as the mirror. No annealing process was used after the mirror deposition in order to maintain a high reflectivity at that surface. In addition to the initial Au layer, a Cr layer was also deposited as a diffusion barrier to prevent degradation of the mirror quality from the diffusion of indium used in subsequent steps. A second Au layer was then deposited to function as a wetting layer to better adhere to the indium. Fig. 4(c) shows, schematically, the mirror deposited on the textured Al_{0.52}In_{0.48}P layer along with the point contacts. Indium was used as the bonding medium to secure the GaAs wafer to the Si carrier wafer. A Ti/Au film deposited onto the Si carrier wafer using E-beam evaporation prior to the bonding process acts as a wetting layer for better adhesion to the indium, and also as the back probing pad of the solar cells during the final measurement. Indium shot was melted across the surface of the Si wafer using a hot plate at 210 °C, and the GaAs wafer was mounted on the indium, mirror side down. Fig. 4(d)shows the structure after the sample was bonded to the Si carrier wafer.

The GaAs substrate was then removed through a chemical-etching process over a period of approximately 3 h using an NH₄OH/H₂O₂ solution. The Al_{0.9}Ga_{0.1}As etch-stop layer prevented the absorber from being removed. This Al_{0.9}Ga_{0.1}As etch-stop layer was then removed using an HF/H₂O solution as shown in Fig. 4(e). The top Ti/Pt/Au contact grids were deposited using a photolithography, E-beam evaporation, and lift-off process. The exposed GaAs contact layer was then removed using a citric acid etchant to expose the In_{0.49}Ga_{0.51}P window layer underneath, as shown in Fig. 4(f). The individual devices were isolated using chemical etching between the mesas, as shown in Fig. 4(g), with HNO₄/HCl/H₂O used as the etching solution. The final cell geometries are $0.3 \text{ mm} \times 0.3 \text{ mm}$, $0.6 \text{ mm} \times 0.6 \text{ mm}$, and $1 \text{ mm} \times 1 \text{ mm}$. The thickness-optimized MgF₂/ZnS layers for the AR coating were then deposited using a standard



FIG. 4. Schematic flow of the device growth and fabrication processes: (a) MOCVD growth of the double-heterostructure PN junction device with an $A_{0.52}In_{0.48}P$ light scattering layer on a GaAs substrate; (b) metals deposition for point contacts; (c) deposition of Au mirror; (d) flip-chip bonding to a Si carrier substrate; (e) removal of GaAs substrate; (f) top contact deposition; (g) mesa etching; and (h) MgF₂/ZnS AR coating deposition.



FIG. 5. Measured reflectance of a 97 nm $MgF_2/68$ nm ZnS AR coating on a blank GaAs calibration wafer.

photolithography, thermal evaporation, and lift-off process. Fig. 4(h) shows the final device schematic including the AR coating ready for test.

A custom built thermal evaporation tool was used for the AR coating deposition. During deposition, a blank GaAs calibration wafer was placed alongside the actual devices to permit straightforward measurement of the AR coating thickness and reflectance. The MgF₂ and ZnS thicknesses on the calibration wafer were measured to be 97 nm and 68 nm by Filmetrics F-40 reflectance measurement, respectively. The reflectance curve of the calibration wafer is shown in Fig. 5. This reflectance curve is then weighted against the solar spectrum and integrated below the GaAs bandgap wavelength as described above. A total reflectance (R_{total}) of 3.77% was obtained for this MgF₂/ZnS bilayer stack. Due to the limitation of the custom tool used for AR coating deposition, there is a large spatial variation of the deposited film thickness. As the calibration wafer and the solar cell devices are at different locations, the final AR coating thicknesses on the solar cell samples may be slightly different from that of the calibration wafer.

DEVICE CHARACTERIZATION AND ANALYSIS

The current-voltage (I-V) characteristics of the processed solar cell devices were measured using an Oriel Class-A solar simulator (AM 1.5G, 0.1 W/cm^2) and a Keithley 2400 source meter. The spectral reflectance measurements of the MgF₂/ZnS AR coating were carried out at normal incidence using a Perkin Elmer Lambda 18 UV-Vis spectrometer. The external quantum efficiencies (EQE) of the devices were measured at room temperature using a Newport QE/IPCE measurement setup.

Because the devices were not thermally annealed after the front-contact deposition to avoid degradation of the Au mirror and the melting of the indium, the device structure utilizes a 6 nm highly doped $(1 \times 10^{19} \text{ cm}^{-3}) \text{ p}^+\text{-}\text{GaAs}$ layer on the 200 nm p-GaAs as a non-alloyed ohmic contact layer. The contact resistance of the top p-contact grids was characterized using the transfer length method (TLM). The I–V characteristics measured between two adjacent electrodes with different spacing $(20 \,\mu\text{m}, 40 \,\mu\text{m}, 60 \,\mu\text{m}, 80 \,\mu\text{m}, \text{and})$



FIG. 6. Current–voltage curves of TLM contacts with pad spacing distances of 20, 40, 60, 80, and 100 $\mu m.$

 $100 \,\mu\text{m}$) are shown in Fig. 6. The linear I-V curves reveal that the top metal p-GaAs contacts are ohmic with a specific contact resistivity of $6.51 \times 10^{-3} \,\Omega \cdot \text{cm}^2$.

The finished devices have various areas ranging from 0.09 mm^2 to 1 mm^2 as shown in the optical micrograph (Fig. 7). Devices with an area of 0.09 mm^2 and a 9.7% top contact grid coverage were characterized at room temperature under 1 sun AM 1.5G solar spectrum. A typical I-V plot of the 0.09 mm² solar cell is shown in Fig. 8. Short-circuit current densities (J_{sc}) as high as 24.5 mA/cm², open-circuit voltages (V_{oc}) up to 1.00 V, fill factors (FF) of 77.8%, and power conversion efficiencies (η) up to 19.1% were attained. The measured performance is compared with the best achievable performance, both of which are summarized in Table I. The best achievable performance is modeled based on the assumptions of Lambertian scattering on the back side of the cell, a back mirror with 100% reflectivity, an AR coating with 2% reflectance, a 2% top contact grid coverage, the best material quality with the longest minority carrier lifetime published, and no series or shunt resistance.

The demonstrated GaAs single-junction solar cells with a 300 nm absorber have a maximum achievable J_{sc} of



FIG. 7. Optical micrograph of fabricated solar cells possessing different areas.



FIG. 8. Measured room-temperature I-V characteristics of the GaAs solar cells under 1 sun (AM 1.5G). Inset: calculated short-circuit current density (J_{sc}) as a function of Phong exponent (m).

 30.7 mA/cm^2 . The best device with a 300 nm absorber tested has a measured J_{sc} of 24.5 mA/cm². The causes of this discrepancy from the maximum achievable J_{sc} are: (1) the optical scattering in the actual devices is not as efficient as Lambertian scattering; (2) the Au mirror has less than 100%reflectivity; (3) the AR coating has a larger reflectance (3.8%) than that used in the model (2%); and (4) the metal grid coverage (9.7%) is much higher than that used in the model (2%). If a 2% metal grid shadowing is assumed as in conventional devices and all other factors remain the same, the J_{sc} and power conversion efficiency of these actual devices could reach 26.6 mA/cm² and 20.7%, respectively. The resulting 4.1 mA/cm² difference between the projected and maximum achievable J_{sc} is due in part to non-ideal scattering. The impacts of non-ideal scattering are addressed as below.

Phong's distribution can be used to describe non-ideal scattering deviating from Lambertian scattering.²³ The angular light intensity is proportional to $\cos^{m}(\theta)$, where θ is the angle between the scattered light and the surface normal, and the Phong exponent (m) can be used to describe the angular intensity distribution. Note here that m = 1 corresponds to the Lambertian distribution, and higher values of *m* result in narrower angular intensity distributions. The calculated J_{sc} as a function of *m* value is shown in Fig. 8 inset. The calculation assumes a reflectivity of 95% for the back Al_{0.52}In_{0.48}P/Au mirror, a metal grid shadow area of 9.7%, and a front surface reflection loss of 3.8%. The value of 95% is chosen as an approximation of the reflectivity of the rough Al_{0.52}In_{0.48}P/Au mirror based on the Au reflectivity spectrum and its angular distribution. The calculation results indicate that the Al_{0.52}In_{0.48}P/Au reflectivity averaged against the Lambertian distribution (m=1) at the GaAs band edge

TABLE I. Comparison between the measured and modeled best achievable device performance of $300 \,\mathrm{nm}$ GaAs single-junction solar cell.

	$V_{oc}\left(\mathbf{V}\right)$	J_{sc} (mA/cm ²)	FF (%)	η (%)
Modeled best achievable	1.13	30.7	85.4	29.5
Measured	1.00	24.5	77.8	19.1

wavelength (870 nm) is 94.8%, while the value for the narrowest Phong's distribution $(m = \infty)$ is 94.4%. Fig. 8 inset shows that J_{sc} decreases from ~27 mA/cm² to ~23 mA/cm² as the *m* value increases from 1 to 40, which represents that the scattering gradually deviates from a Lambertian distribution to a Phong's distribution. The *m* value of the Phong's distribution in our device is determined to be 15 by fitting the modeled J_{sc} to the experimental results.

The over 0.1 V difference between the measured and the theoretically predicted, best achievable V_{oc} could be partly due to SRH and interface/surface recombination losses. The impact of interface/surface recombination becomes relatively more important for such a thin absorber. The current low *FF* value of 77.8% is mainly due to the series resistance. A specific series resistivity of $0.5 \ \Omega \cdot \text{cm}^2$ is determined by fitting the deviation of the dark I-V curve from the ideal diode equation at a positive bias voltage of 1.5 V. Poor current spreading in the devices with ultra-thin absorber and window layers, sparse back point contacts, as well as non-annealed top contact grids all contribute to this relatively high series resistance. The *FF* can be potentially improved by using our recently proposed conductive AR coating.²⁴

Fig. 9 shows the EQE of the measured solar cell. The EQE increases from 80% at 400 nm to 90% at 470 nm. Note that the light at a wavelength near 400 nm is mainly absorbed by the In_{0.49}Ga_{0.51}P window layer. The high EQE near 400 nm indicates that the highly efficient extraction of those photo-generated carriers in the window layer is due to the shallow PN junction of the ultra-thin absorber and the extension of the depletion region into the window layer. The maximum value of the EQE is only \sim 90% mainly due to the fact that the top grid has a shadow area of 9.7%. The EQE is close to 65% at 800 nm and above 40% at the bandgap wavelength of 870 nm. The light absorption at 800 nm is calculated to be $\sim 54\%$ for devices with specular reflective back surfaces without light scattering. These calculations assume shadow area of 9.7%, a reflectivity of 95% for the back Al_{0.52}In_{0.48}P/Au mirror, and a reflectance of 2% at 800 nm for AR coating. The 11% increase in the measured EQE compared with the calculated specular light absorption at



FIG. 9. External quantum efficiency as a function of wavelength for the GaAs solar cell.

800 nm indicates that the textured back scattering has resulted in an increase in the EQE at these wavelengths. No interference fringes in the EQE spectrum between 550 nm and 870 nm are observed, indicating that the textured $Al_{0.52}In_{0.48}P$ surface offers reasonably effective light scattering.¹⁷ However, the presence of a lower EQE at the longer wavelengths compared to that at the shorter wavelengths indicates that the reflective back scattering is not as efficient as Lambertian scattering/reflection.

CONCLUSION

Semi-analytical modeling shows that one of the highest potential efficiencies for GaAs single-junction solar cells with conventional material quality can be obtained through the combination of an ultra-thin absorber and a reflective back scattering layer. Ultra-thin, double-heterostructural, single-junction In_{0.49}Ga_{0.51}P/GaAs/In_{0.49}Ga_{0.51}P solar cells, monolithically integrated with a lattice-matched textured Al_{0.52}In_{0.48}P layer coated with a Au mirror have been successfully demonstrated. Open-circuit voltages up to 1.00 V, short-circuit current densities up to 24.5 mA/cm², and a maximum power conversion efficiency of 19.1% are measured under 1 sun AM 1.5G solar radiation. These results were obtained for substrate-removed and flip-chip bonded devices with an ultra-thin 300 nm GaAs absorber, a textured Al_{0.52}In_{0.48}P surface, a MgF₂/ZnS AR coating, and a contact grid layout covering 9.7% of the front surface area. If a more conventional 2% metal grid shadow is assumed, a J_{sc} of 26.6 mA/cm^2 and a conversion efficiency of 20.7% can be expected for these devices. Deviations in device performance from the ideal model are discussed. These initial results demonstrate the feasibility and potential of using an ultra-thin GaAs single-junction solar cell design integrated with a reflective back scattering layer to optimize light management and minimize non-radiative recombination.

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