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Flexible modules using $<70 \ \mu m$ thick silicon solar cells

André Augusto, Kevin Tyler, Stanislau Y. Herasimenka, and Stuart G. Bowden

School of Electrical, Computer and Energy Engineering, Arizona State University, P.O. Box 875706, Tempe AZ 85284, USA

Abstract

Highly flexible modules using thin 153 cm² silicon crystalline cells and transparent fluoropolymer foil are demonstrated. The modules can be flexed 200 times around a bend radius of 4 cm without change in efficiency. The silicon crystalline heterojunction solar cells are $65\pm5 \mu$ m-thick with efficiencies up to 18.4%. Cracks in the solar cells and interconnections that are induced by mechanical stress during module bending are examined using electroluminescence. Two interconnection solutions are discussed: ribbons affixed to the busbars using a conductive adhesive, and indium coated wires directly bonded to the cell fingers. Modules using wire interconnection are found to be highly flexible with efficiencies greatly exceeding existing commercial flexible modules using thin films and have potential applications in light-weight modules for building integrated and portable photovoltaic power.

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1. Introduction

Since 2011 the balance-of-system (BOS) is the leading cost in PV [1,2]. In the period of 2011-2015 \$4 billion were invested in PV innovation, and 17%, was on BOS technology. Mounting and racking systems segment account 7-10% of the total cost of the installed photovoltaic system, with estimated market size of \$6billion [3].

Lightweight modules have the potential to reduce the BOS cost and increase PV deployment [4]. Lighter and more flexible modules enable PV on weight-constrained roofs (e.g. industrial roofs) and their compact size reduces the transportation costs across supply chain. The installation on roofs is less costly, safer, and minimalist mounting setups are available (e.g. adhesive mounts). They are generally more aesthetically pleasing and the fluoropolymer commonly used as front sheet has typically over 95% optical transparency with lower the glare that allows module use where glare is a critical safety issue (e.g. airports). To succeed the modules must improve not only the power-to-weight ratio, but also the power-to-area performance. Traditionally lightweight and flexible modules are associated

with organic and thin films deposited over flexible substrates [5,6]. In these devices the power-to-area performance is low. In recent years, new players [7-10] are pursuing a different strategy that uses high efficiency crystalline silicon solar cells instead of using thin film technologies. To reduce weight, a thin transparent polymer replaces the front glass and the structural component is a rigid back sheet that replaces the aluminium frame. The weight savings are up to 85% over a typical glass/tedlar/aluminium frame module. We estimate that our modules will be 10 times lighter per watt-peak than conventional crystalline silicon modules while maintaining similar power-to-area performance. However, the absence of glass and aluminium frame makes the module structurally less rigid and also more vulnerable to mechanical/environmental hazards. The most pressing challenges are [4,11]: cracks in the solar cells and ribbons due to mechanical stress as a result of bending during manufacturing and installation, cracks as result of increased thermally induced stress due to the mismatch of thermal expansion coefficient of the polymer components and the silicon cells, mechanical impact due to hail, corrosion due to increased moisture permeation through the polymeric front/back-sheet and, and encapsulant degradation due to increased moisture ingress and UV radiation. In this manuscript, we address many of these issues by using thin flexible crystalline silicon cells that bend under stress rather than break like conventional cells.

The availability of flexible and high performance solar cells based on a cost competitive and high manufacturing throughput technology provide an opportunity for new range of applications - particularly in building integration PV (BIPV) [12] and portable PV devices. We test the flexibility of the cells and interconnections beyond the requirements for semi-rigid lightweight modules to understand the likely failure mechanism in long term and extreme use.

2. Experiment

2.1. Solar cells

In thin wafers, nearly all minority carriers reach one of the surfaces within their lifetimes and the quality of the surface passivation is critical to mitigate recombination of the carriers [13]. The crystalline silicon heterojunction (SHJ) architecture provides high quality passivation by using hydrogenated intrinsic amorphous silicon (a-Si:H) [14] as buffer layer separating the absorber from highly recombination-active (ohmic) contacts [15]. The cells are prepared on five-inch n-type Czochralski wafers with 3-4 Ω .cm resistivity and initial thickness of 145 μ m. The wafers are thinned to 65±5 μ m and textured using alkaline wet etching, followed by wet chemical cleaning. The heterojunction is formed using plasma enhanced chemical vapor deposition (PECVD) to grow intrinsic and doped a-Si:H layers (7-10 nm) on both sides of the wafer as shown in Fig. 1 below. Hydrogen plasma treatment improves the performance of the intrinsic layer [16] by restructuring the film during growth. Indium tin oxide (ITO) is sputtered on both sides of the wafer, and silver on the rear as a mirror and rear contact. The front metallization is screen printed silver. The collecting junction can be either in the front or rear side of the cell, greatly simplifying the interconnection of the cells and the encapsulation processes.



Fig. 1. SHJ cell with front and rear junction. The cells have comparable performances.

2.2. Modules

Two distinctive cell interconnections are examined: traditional ribbons affixed to the busbars using a Z-axis conductive adhesive, and indium coated wires. The wires induce lower mechanical stress on the cells, reduce the shading area, and require less silver paste during the previous printing step [17]. Fig. 2 shows the cell encapsulation with ribbons and wires. The module with wires has an extra polyethylene layer to force the wires against the fingers of the cell. Indium has a melting point of 156 $^{\circ}$ C that is close to the lamination temperature of 150 $^{\circ}$ C and allows the indium to melt during lamination, creating a good electrical contact. The lamination was performed at 150 $^{\circ}$ C for 5 min.



Fig. 2. The black squares (left) represent the ribbons and the black circles (right) the indium-wires. The ribbons were affixed to the busbars using a Z-axis conductive adhesive, and the the wires were fasted against the fingers with the help of the polyethylene layer. In the wires configuration there is no need for busbars, but in this case the busbars were kept to measure the electrical properties before encapsulation. The fluoropolymer is 200 µm-thick and the polyethylene is 80 µm-thick. The ribbons thickness is 130 µm and the wires thickness is 200 µm.

To study the flexibility of the modules, one-cell modules were manufactured with three different interconnections: ribbons 4 mm-wide, ribbons 2 mm-wide, and indium coated wires 0.2 mm-diameter (effective shading 0.15 mm). The ribbons are tin coated and they are 0.13 mm-thick. The total shading area due to wiring is 1.5%, and 3.7% due to busbars. To compare the electrical properties of the cell and module all cells are printed with busbars. In the final design of wire interconnected modules the busbars on the cells (and their attendant shading loss) would be eliminated. The modules are stress tested using the same procedure for testing thin film modules by flexing around cylinders with three radiuses: 8 cm, 6 cm, and 4 cm. The modules are flexed 100 times for each cylinder. The 6 cm and 4 cm tests are very extreme for applications like lightweight modules, but are useful to understand the limits of the modules, and the range of applications. Electroluminescence (EL) imagining is used to inspect the cell cracking, and the electrical performance is measured after each bending step with a IV flash tester.

Alternating front-junction and rear-junction SHJ cells enables direct series interconnection of equivalent sides, i.e. front-to-front and back-to-back of neighbouring cells, as shown in Fig.3. With this arrangement the module manufacturing process is more streamlined and the resulting package is fully planar. The cells can also be assembled closer together and the yield of the module packaging process is improved. The challenge with this solution is to have cells with similar electrical properties, especially in terms of current [18]. The front and rear junction cells in this study have electrical properties are very similar [19] as the cells are well passivated by the amorphous silicon and the diffusion length of the minority carriers is high in the n-type base. As in the case of the single cell module, the performance of the interconnected cells is examined using electroluminescence.



Fig. 3. Front-to-front and back-to-back direct connection of thin cells with front and rear junction.

3. Results and discussion

Thin cells (60 μ m-thick) have lower generation current (as measured by integration of the QE) than equivalent thicker cells (125 μ m-thick) due to poor absorption of silicon in the infrared. The losses in measured generation current are 1.2±0.25 mA cm⁻². The external quantum efficiency (EQE) shows that the cells response starts to diverge for wavelengths over 950 nm, Fig. 4(a). In silicon, the absorption depth [20] for wavelengths greater than 950 nm is over 60 μ m, and for 1000 nm is over 150 μ m, Fig. 4(b).



Fig. 4. (a) EQE of 125 µm and 60 µm-thick cell, with generation currents of 39.7 mA cm⁻² and 38.7 mA cm⁻², respectively. (b) Absorption depth in silicon.

The fluoropolymer encapsulation introduces losses in the generation current of 1.6 ± 0.2 mA cm⁻² with most of the losses are in the UV region as shown in Fig. 5. The UV absorption is well known for glass and ethylene vinyl acetate (EVA) encapsulation. There is also an additional loss due to the anti-reflection coating of the cell not being optimized for encapsulation.



Fig. 5. EQE of 60 μ m-thick cell before and after the encapsulation with fluoropolymer. The generations currents are 38.7 mA cm⁻² and 36.9 mA cm⁻², respectively.

The long wavelength losses due to the cell thickness are directly seen in measured short-circuit current losses. However, the flash light has poor emission for wavelengths below 400 nm so the detrimental effect of the fluoropolymer is not so apparent in the short-circuit current measurement. The short-circuit decrease after



Fig. 6. Electrical properties of 30 cells and the respective one-cell module. 15% of the cells were encapsulated using wires, and the rest of the cells were evenly distributed between 4 mm and 2 mm wide ribbons. The area of the modules was adjusted to correct for the extra shading caused by the ribbons and wires. Process variation causes the variability in plots and there was no significant difference between front and rear junction cells or the efficiency of the modules using ribbons and wires.

One of the main goals of this experiment was to understand how the interconnections affect the flexibility of the module using the procedure outlined in the previous section. The module using the wider ribbons (4 mm wide) cracked after flexing around the 6 cm radius cylinder. When the width of the ribbon was reduced to 2 mm, the module was flexed for 100 times around the 6 cm radius cylinder. The EL images in Fig. 7, demonstrate that the cracks initiate next to the ribbons. When the ribbons are replaced by 0.2 mm-diameter indium coated wires, the module can be flexed 200 times to bend radius of 4 cm without change in efficiency.



Fig. 7. EL images of one-cell modules: (a) Ribbon 4 mm-wide, the module cracked after flexed to 6 cm bend radius. (b) Ribbon 2 mm-wide, the module cracked after flexed to 4 cm bend radius. (c) Indium coated wires, the module didn't crack after flexed to 4 cm bending radius. The tested cells were 100 cm² and 153 cm².

Fig. 8 shows the EL image of the first 60 μ m-thick two-cell module produced in our lab using indium coated wires and the planar connection scheme utilizing front and rear junction cells. The EL images shows that the wires have correctly bonded to the underlying screen printed fingers as evidenced by the lack of bright regions adjacent to the wires. Across the cells there are multiple dark regions of lower performance. These dark regions are also present before the cells is encapsulated and largely come from silicon surface defects before passivation with a-Si. For instance, the cell on the left shows a vertical line pattern in the EL image unconnected with the wires and the cell on the right shows a horizontal pattern of lines. In both cases the line pattern originates from wafer sawing and it is remarkable that etch process preserves the pattern despite the considerable thinning of the wafers. While the dark regions are quite prominent in the EL images they do not have a large effect on cell performance as evidenced by the high V_{OC} of these cells.



Fig. 8. EL image of two-cells module using 153 cm² and 60 µm-thick cells. The busbars are not taking any role in the measurement.

4. Conclusions

Flexible one-cell modules show efficiencies very close to the original cell. The best flexible module uses cells 60 μ m-thick with an efficiency of 18.4%. The flexibility of the module is constrained by the type of interconnection and not by the solar cell flexibility. Cracks are initiated next to the ribbons that are affixed to the busbars. Using 0.2 mm-diameter indium coated wires, modules can be flexed 200 times to bend radius of 4 cm without change in efficiency or damage to the cells. The electroluminescence images of the 60 μ m-thick two-cell module is a good indication about the quality of the direct front-to-front series connection between front and rear junction cells.

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